

IN THE CLAIMS:

Please amend the claims of this application so as to read as follows:

1. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:
 - an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;
 - a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
 - a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;
- the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

the apparatus further comprising:

a supplementary capacitance drive circuit for driving the supplementary capacitance lines based on a voltage applied to the counter electrode so that a predetermined potential difference between the voltage applied to the counter electrode and a voltage applied to the pixel electrodes which voltages are different from each other is always maintained when any of the pixel electrodes and supplementary capacitances leaks.

2. (Previously Presented) The active-matrix liquid crystal display apparatus of claim 1, wherein a display mode of the liquid crystal display apparatus is normally-white and the supplementary capacitance drive circuit drives the supplementary capacitance so that a potential difference not less than a threshold voltage of the liquid crystal is maintained between the pixel electrodes and the counter electrode.
3. Canceled, without prejudice.

4. (Previously Presented) The active-matrix liquid crystal display apparatus of claim 1,

wherein the supplementary capacitance lines are separated from every scanning electrode line to which the switching element for switching-driving a pixel potential difference connected through the supplementary capacitance is connected at the intersection, and the supplementary capacitance drive circuit drives the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line driven at a stage preceding the scanning electrode line.

5. (Previously Presented) The active-matrix liquid crystal display apparatus of claim 2,

wherein the supplementary capacitance lines are separated from every scanning electrode line to which the switching element for switching-driving a pixel potential difference connected through the supplementary capacitance is connected at the intersection, and the supplementary capacitance drive circuit drives the supplementary capacitance lines with a polarity being reversed every time an on signal is input to the scanning electrode line driven at a stage preceding the scanning electrode line.

6. Canceled, without prejudice.

7. (As Originally Filed) The active-matrix liquid crystal display apparatus of claim 1,

wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.

8. (As Originally Filed) The active-matrix liquid crystal display apparatus of claim 2,
wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.
9. Canceled, without prejudice.
10. (As Originally Filed) The active-matrix liquid crystal display apparatus of claim 4,
wherein the switching element and the pixel electrode are disconnected from each other at a pixel where the leakage between the pixel electrode and the supplementary capacitance line occurs.
11. Canceled, without prejudice.
12. Canceled, without prejudice.
13. Canceled, without prejudice.
14. Canceled, without prejudice.
15. Canceled, without prejudice.
16. Canceled, without prejudice.

17. Canceled, without prejudice.
18. Canceled, without prejudice.
19. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:
an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrodes via the switching elements;
a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;
the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

the apparatus further comprising:

a supplementary capacitance drive circuit for driving the supplemental capacitance lines based on a voltage applied to the counter electrode so that a predetermined potential difference between the voltage applied to the counter electrode and a voltage applied to the pixel electrodes, which voltages are different from, each other, is always maintained.

20. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:
 - an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;
 - a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
 - a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;
 - the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

the apparatus further comprising:

a supplementary capacitance drive circuit

including a reference input maintained at the same potential as that of the common electrode for driving the supplementary capacitance lines based on the reference input so that a predetermined potential difference between a voltage applied to the counter electrode and a voltage applied to the pixel electrodes which voltages are different from each other, is always maintained when any of the pixel electrodes and supplemental capacitances leaks.

21. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:

an active-matrix substrate including a

plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;

a counter electrode substrate including a

counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;

a liquid crystal sandwiched between the

active-matrix substrate and the counter electrode substrate;

the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines,

the apparatus further comprising:

a supplementary capacitance drive circuit for outputting to the supplementary capacitance lines, based on a voltage applied to the counter electrode, a voltage which (i) always has a predetermined potential difference from the voltage applied to the counter electrode and (ii) is different from the voltage applied to the counter electrode when any of the pixel electrodes and supplementary capacitances leaks.

22. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:
 - an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;
 - a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
 - a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;

the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines, the apparatus further comprising:
a supplementary capacitance drive circuit for outputting to the supplementary capacitance lines, based on a voltage applied to the counter electrode, a voltage which (i) always has a predetermined potential difference from the voltage applied to the counter electrode and (ii) is different from the voltage applied to the counter electrode.

23. (Previously Presented) An active-matrix liquid crystal display apparatus comprising:
an active-matrix substrate including a plurality of scanning electrode lines, a plurality of data electrode lines, pixel electrodes and switching elements, the pixel electrodes being respectively connected to intersections of the plurality of scanning electrode lines and the plurality of data electrode lines via the switching elements;
a counter electrode substrate including a counter electrode formed thereon, the counter electrode being opposed to the pixel electrodes;
a liquid crystal sandwiched between the active-matrix substrate and the counter electrode substrate;

the active-matrix substrate further including supplementary capacitance lines which are formed in parallel to the scanning electrode lines, and supplementary capacitances for holding display data which are connected between the pixel electrodes and the supplementary capacitance lines, the apparatus further comprising:

a supplementary capacitance drive circuit including a reference input maintained at the same potential as that of the common electrode for outputting to the supplementary capacitance lines, based on a reference input, a voltage which (i) always has a predetermined potential difference from a voltage applied to the counter electrode and (ii) is different from the voltage applied to the counter electrode, when any of the pixel electrodes and supplementary capacitances leaks.